

EAST - [10092669.wsp:1]

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DBs USPAT Plurals

Default operator: OR Highlight all hit terms initially

13 and (vco or "voltage controlled oscillator")

Drafts

- BRS:
- Pending
- Active**
 - L6: (2495) (calibrat\$3 near8 voltage) same temperature
 - L7: (236) L6 and (vco or "voltage controlled oscillator")
 - L8: (94) L7 and (calibrat\$3 near8 oscillator)
 - L9: (47) L8 and frequency
 - L10: (16) L9 and (wireless or cellular or mobile)
 - L11: (2) 10 and "in phase"
 - L12: (884) (calibrat\$3 near5 voltage) near5 temperature
 - L13: (70) 12 and "calibration voltage"
 - L14: (10) 13 and (vco or "voltage controlled oscillator")
 - L15: (6) 13 and (vco or "voltage controlled oscillator")
- Failed
- Saved**
 - S9: (15) S8 and divider
 - S1: (65506) "455"/\$.ccls.
 - S12: (1) S10 and (adjust\$3 near5 frequency)
 - S4: (47) S3 and (vco or "voltage controlled oscillator")

BRS form IS&R form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor
2	<input type="checkbox"/>	<input type="checkbox"/>	US 7062229 B2	20060613	17	Discrete amplitude calibration of oscillators in frequency	455/76	455/260; 455/75		Dunworth; Jeremy D. al.
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6954706 B2	20051011	13	Method for measuring integrated circuit processor	702/65	702/124; 702/57;		Poirier; Christopher A et al.
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6316991 B1	20011113	8	Out-of-calibration circuits and methods and systems using	327/543	327/407; 327/540;		Muyshondt; Gabriel Patrick et al.
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5315232 A	19940524	41	Electric field measuring system	324/72	324/457		Stewart; Michael F.

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☐ Drafts
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☐ Failed

☒ L1: (2495) (calibrat\$3 near8 voltage) same temperature
☒ L2: (236) 1 and (vco or "voltage controlled oscillator")
☒ L3: (94) 2 and (calibrat\$3 near8 oscillator)
☒ L4: (94) 3 and frequency
☒ L5: (47) 3 and frequency
☒ L6: (16) 5 and (wireless or cellular or mobile)
☒ L7: (1) ("6137372").PN.
☒ L8: (0) 5 and "proportional to absolute temperature voltage"
☒ L9: (0) 1 and "proportional to absolute temperature voltage"
☒ L10: (0) "proportional to absolute temperature voltage"
☒ L11: (0) 4 and ("lower calibration" and "higher calibration")
☒ L12: (0) 1 and ("lower calibration" and "higher calibration")
☒ L13: (6) 1 and ((lower near3 calibration) and (higher near3 calibration))

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DBs US-PG Pub: USPAT: EPO ☒ Plurals

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1 and ((lower near3 calibration) and (higher near3 calibration))

BRS form IS&R form Image Text HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20030224749 A1	20031204	44	Semiconductor integrated circuit device for communicat	455/252.1	455/195.1;		Uozumi, Toshiya et al.
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20030184459 A1	20031002	95	Devices and methods for calibrating amplifier stages an	341/120	455/196.1		Engl, Bernhard
3	<input type="checkbox"/>	<input type="checkbox"/>	US 7103337 B2	20060905	41	PLL circuit having a multi-band oscillator and com	455/255	331/176;		Uozumi, Toshiya et al.
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6406806 B1	20020618	11	Fuel cell voltage monitoring and system control	429/13	429/23		Keskula, Donald H. et al
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5909963 A	19990608	8	Method for determining a temperature of a body by mea	374/175			Weiss, Michel et al.
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5741071 A	19980421	7	Radiometer for the determination of a temperatur	374/175			Weiss, Michel et al.


☐ Drafts

☐ BRS:

☐ Pending

☐ Active

- ☐ L1: (2495) (calibrat\$3 near8 voltage) same temperature
- ☐ L2: (236) 1 and (vco or "voltage controlled oscillator")
- ☐ L3: (94) 2 and (calibrat\$3 near8 oscillator)
- ☐ L4: (94) 3 and frequency
- ☐ L5: (47) 3 and frequency
- ☐ L6: (16) 5 and (wireless or cellular or mobile)

☐ Failed

☐ Saved

- ☐ S9: (15) S8 and divider
- ☐ S1: (65506) "455"/\$.ccls.
- ☐ S12: (1) S10 and (adjust\$3 near5 frequency)
- ☐ S4: (47) S3 and (vco or "voltage controlled oscillator")
- ☐ S2: (229) S1 and (calibrat\$3 near8 oscillator)
- ☐ S3: (63) S2 and (PLL or "phased lock loop")

 US-PGPUB:USPAT:EPO ☒ Bursals

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5 and (wireless or cellular or mobile)

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current Ref	Inventor
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 7098748 B2	20060829	10	Integrated CMOS high precision piezo-electrically dri	331/176	331/65; 331/66	Schmidt; Dominik J.
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6850125 B2	20050201	7	Systems and methods for self-calibration	331/65	331/66; 331/68	Norman; Robert D et al
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6549078 B1	20030415	8	Method and system for directly modulating a voltage	331/17	327/156; 327/159	Sridharan; Guruswami M. et al.
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6545547 B2	20030408	17	Method for tuning a VCO using a phase lock loop	331/16	331/17	Fridi; Ahmed Reda et al.
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5892408 A	19990406	27	Method and system for calibrating a crystal oscillator	331/44	331/176; 331/66	Binder; Yehuda
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4746879 A	19880524	20	Digitally temperature compensated voltage-controll	331/44	331/116F	Ma; John Y. et al.

Ready

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